

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:)	
)	Group Art Unit: Unassigned
D. Heim et al.)	
)	Examiner: Unassigned
Application No. Unassigned)	
)	Atty. Docket No. HIT1P077/
)	HSJ920040021US1
Filed: Herewith)	
)	Date: March 31, 2004
For: TOPOGRAPHICALLY DEFINED THIN)	
FILM CPP READ HEAD FABRICATION)	
)	

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on March 31, 2004.

Signed: _____

Erica L. Farlow

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §§ 1.56 AND 1.97(b)**

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

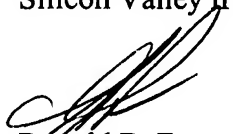
Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§ 1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is believed to be filed before the mailing date of a first Office Action on the merits. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-2587 (Order No. HSJ920040021US1/HIT1P077).

Respectfully submitted,
Silicon Valley IP Group, PC



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Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty. Docket No. HIT1P077/ HSJ920040021US1 Applicant: D. Heim et al. Filing Date: Herewith	Application No.: Unassigned Group Art Unit: Unassigned

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	1	2003/0231436	12/18/2003	Nishiyama	360	324.1	06/13/2003
	2	2003/0203238	10/30/2003	Saito	428	694	04/23/2003
	3	2003/0143431	07/31/2003	Hasegawa	428	692	01/21/2003
	4	5,285,339	02/08/1994	Chen et al.	360	113	02/28/1992
	5	5,018,037	05/21/1991	Krounbi et al.	360	113	10/10/1989
	6						
	7						
	8						
	9						
	10						
	11						
	12						
	13						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	14	9526547	1995-10-05	WO	G11B	5/00		
	15							
	16							
	17							
	18							
	19							
	20							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	21	"Method of Making Submicron Channel MOSFET", IBM Technical Disclosure Bulletin, pp 3407-3408, December 1981
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.